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PATENT & TRADEMARK OFFICE

Atty. Docket No. 02986.P035

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of:)
John M. Beardslee, et al.)
Application No. 09/724,585)
Filed: November 28, 2000)
For: METHOD AND SYSTEM FOR)
DEBUGGING AN ELECTRONIC SYSTEM)

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Examiner: Thangavelu, K.
Art Unit: 2123
Confirmation No. 7620

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Sir:

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Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 8/16, 2005


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<p>Substitute for Form 1449/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>				Complete if Known	
				Application Number	09/724,585
				Filing Date	11/28/2000
				First Named Inventor:	John M. Beardslee
				Art Unit	2123
				Examiner Name	Thangavelu, K.
Sheet	1	of	7	Attorney Docket No.	02986.P035

U.S. PATENT DOCUMENTS

Exmnr Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
		US- 6,587,995	B1	07/01/2003	Duboc et al.	
		US- 6,779,145	B1	08/17/2004	Edwards et al.	
		US- 6,785,854	B1	08/31/2004	Jaramillo et al.	
		US- 6,789,217	B2	09/07/2004	Slaugh et al.	
		US- 6,791,352	B2	09/14/2004	Verdoorn et al.	
		US- 6,795,963	B1	09/21/2004	Andersen et al.	
		US- 6,799,128	B2	09/28/2004	Duff et al.	
		US- 6,822,474	B2	11/23/2004	Chaudhari	
		US- 6,823,224	B2	11/23/2004	Wood et al.	
		US- 6,826,717	B1	11/30/2004	Draper et al.	
		US- 6,839,654	B2	01/04/2005	Röllig et al.	
		US- 6,839,874	B1	01/04/2005	Fang	
		US- 6,859,892	B2	02/22/2005	Bolding et al.	
		US- 6,868,376	B2	03/15/2005	Swoboda	
		US- 6,894,530	B1	05/17/2005	Davidson et al.	

FOREIGN PATENT DOCUMENTS

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		Country Code ³	Number ⁴				
		WIPO	00/043884	A1	07/27/2000	Wenzel et al.	
		WIPO	01/063434	A1	08/30/2001	Barry	
		WIPO	01/071876	A1	09/27/2001	Ricchetti et al.	

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Examiner Name	Thangavelu, K.

Sheet 2 **of** 7 **Attorney Docket No.** 02986.P035

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INFORMATION DISCLOSURES STATEMENT BY APPLICANT

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NON PATENT LITERATURE DOCUMENTS

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		Ableidinger et al., "Multi-Core Embedded Debug for Structured ASIC Systems", DesignCon 2004, February 2004, 23 pgs.	
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Art Unit	2123
Examiner Name	Thangavelu, K.

Sheet	4	of	7	Attorney Docket No.	02986.P035
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NON PATENT LITERATURE DOCUMENTS

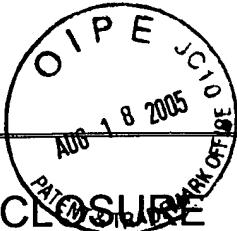
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
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		First Silicon Solutions, Inc., "Preliminary Technical Data for FS2 MED System for SoC Multi-Core Embedded Debug", http://www.fs2.com , February 2004, 2 pgs.	
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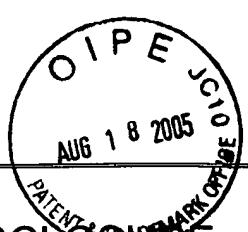
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		Xilinx, Inc., "ChipScope Pro Software and Cores User Guide", UG029, Version v7.1, February 2005, 122 pgs.	
		Yang et al., "Extraction Error Modeling and Automated Model Debugging in High-Performance Low Power Custom Designs", Date Conference 2005, March 2005, 6 pgs.	

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